



# LPC1100 CPU Support Package Guide

**Version: 3.1**





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LPC11E14FBD48/401 .....	103
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LPC11E37HFBD64/401 .....	110
LPC11E66JBD48 .....	111
LPC11E67JBD100 .....	112
LPC11E67JBD48 .....	113
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LPC11U24FBD48/301 .....	127
LPC11U24FBD48/401 .....	128
LPC11U24FBD64/401 .....	129
LPC11U24FET48/301 .....	130
LPC11U24FHI33/301 .....	131
LPC11U24FHN33/401 .....	132
LPC11U34FBD48/311 .....	133
LPC11U34FBD48/421 .....	134
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LPC11U34FHN33/421 .....	136
LPC11U35FBD48/401 .....	137
LPC11U35FBD64/401 .....	138
LPC11U35FET48/501 .....	139
LPC11U35FHI33/501 .....	140
LPC11U35FHN33/401 .....	141
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# LPC1100 Support Package

This guide describes the following features of the LPC1100 CPU support package:

- [How to create LPC1100 projects](#)
- [How to open LPC1100 sample projects](#)
- [LPC1100 specific project properties](#)
- [LPC1100 specific project templates](#)
- [Supported LPC1100 devices](#)

# Creating LPC1100 Projects

## Creating an LPC1100 C/C++ executable project

To create a new minimal C/C++ executable project:

- Select the **File > New > New Project** menu item.
- Select the **A C/C++ executable for NXP LPC1100** project template.
- Set the required project name and location directory.
- Click **Next**.
- If required, change any of the default project settings.
- Click **Finish** to create the project.

## Creating an LPC1100 library project

To create a new library project:

- Select the **File > New > New Project** menu item.
- Select the **A library for NXP LPC1100** project template.
- Set the required project name and location directory.
- Click **Next**.
- If required, change any of the default project settings.
- Click **Finish** to create the project.

## Creating an LPC1100 externally built executable project

To create a new project that will allow you to debug an existing externally built executable file:

- Select the **File > New > New Project** menu item.
- Select the **An externally built executable for NXP LPC1100** project template.
- Set the required project name and location directory.
- Click **Next**.
- Set the **Load File** project property to point to the executable file you want to download and debug.
- If required, change any of the other default project settings.
- Click **Finish** to create the project.

## Creating an LPC1100 CrossWorks Tasking Library executable project

To create a new C/C++ executable project configured to use the CrossWorks Tasking Library:

- Select the **File > New > New Project** menu item.
- Select the **A CrossWorks Tasking Library executable for NXP LPC1100** project template.
- Set the required project name and location directory.
- Click **Next**.



- If required, change any of the other default project settings.
- Click **Finish** to create the project.

### Creating an LPC1100 assembly code only executable project

*Please note, this template does not add any C/C++ startup code or libraries and is therefore not suitable for creating projects that include C/C++ code.*

To create a new assembly code only executable project without:

- Select the **File > New > New Project** menu item.
- Select the **An assembly code only executable for NXP LPC1100** project template.
- Set the required project name and location directory.
- Click **Next**.
- If required, change any of the other default project settings.
- Click **Finish** to create the project.

# Opening LPC1100 Sample Solutions

## LPC1100 Samples Solution

This solution contains general sample projects that run on LPC1100 devices. To open the LPC1100 Samples Solution:

- Select the **Tools > Show Installed Packages** menu item.
- Select the **NXP LPC1100 CPU Support Package** link.
- Select the **Samples Solutions > LPC1100 Samples Solution** link.

## LPC1100 CMSIS-DSP Samples Solution

This solution contains sample projects that use the CMSIS-DSP library running on LPC1100 devices. To open the LPC1100 CMSIS-DSP Samples Solution:

- Select the **Tools > Show Installed Packages** menu item.
- Select the **NXP LPC1100 CPU Support Package** link.
- Select the **Sample Solutions > LPC1100 CMSIS-DSP Samples Solution** link.

# LPC1100 Project Properties

Projects creating using the project templates in this support package have the following device specific project properties:

## Heap Size

The heap size is set to be 256 bytes when a project is created. The heap size can be modified using the **Heap Size** project property.

## Section Placement

You can select the memory configuration you require using the **Section Placement** project property.

For LPC1100 projects, the set of placements are:

- **Flash** - The application runs in internal Flash memory (*default*).
- **Flash Vectors In RAM** - The application runs in internal Flash memory and exception vectors are copied to RAM memory.
- **Flash Copy To RAM** - The application starts in internal flash and copies itself to run from internal RAM memory.
- **RAM** - The application runs from internal RAM memory only.

## Stack Sizes

The main stack size is set to be 256 bytes when a project is created.

The process stack size is set to be 0 bytes when a project is created.

The main and process stack sizes can be modified using the **Main Stack Size** and **Process Stack Size** project properties.

To change the location of the stacks, edit the section placement file and place the `.stack` and `.stack_process` sections as required.

## Startup From Reset

By default, the application will only startup from power-on/reset in *Release* configuration. This acts as a safety net in case you accidentally download a program in FLASH during development that crashes and prevents the debugger from taking control of the target over the debug interface thus rendering the device unusable.

For LPC1100 projects, the **Startup From Reset** project property can be set to one of the following:

- **No** - The application will not startup from reset.

- **Release Only** - The application will only startup from reset when built in *Release* configuration (*default*).
- **Yes** - The application will always startup from reset.

## Target Processor

Once a project has been created you can target different devices by modifying the **Target Processor** project property. See the [LPC1100 Devices](#) section for details on the files, preprocessor definitions and macro definitions used when a device is selected.

## LPC1100 Project Templates

The project template system simplifies the creation of new projects with the IDE, it also system makes it easy to create new projects with a text editor or script. All that needs to be specified is the project name, the support packages that the project is dependent on, the target processor and the source files you want to add to the project. For example, create a file called *example.hzp* with the following contents:

```
<!DOCTYPE CrossStudio_Project_File>
<solution Name="Example Solution">
  <project Name="Example Project" template_name="LPC1100_EXE">
    <configuration Name="Common" package_dependencies="LPC1100" Target="LPC11E68JBD100" />
    <folder Name="Source Files">
      <file file_name="file1.c" />
      <file file_name="file2.c" />
    </folder>
  </project>
</solution>
```

You can also add any other property settings that the project requires such as preprocessor definitions or include paths using the property save name, for example:

```
<!DOCTYPE CrossStudio_Project_File>
<solution Name="Example Solution">
  <project Name="Example Project" template_name="LPC1100_EXE">
    <configuration Name="Common" package_dependencies="LPC1100" Target="LPC11E68JBD100"
      c_preprocessor_definitions="MYDEF1=1;MYDEF2=TWO" c_user_include_directories="$(ProjectDir)/
      include1;$(ProjectDir)/include2" />
    <folder Name="Source Files">
      <file file_name="file1.c" />
      <file file_name="file2.c" />
    </folder>
  </project>
</solution>
```

### Available LPC1100 project templates

Template Name	Template Description
LPC1100_ASM_EXE	LPC1100 Assembly Code Only Executable
LPC1100_CTL_EXE	LPC1100 CTL Executable
LPC1100_EXE	LPC1100 C/C++ Executable
LPC1100_EXT_EXE	LPC1100 Externally Built Executable
LPC1100_LIB	LPC1100 Library

# LPC1100 Devices

This package supports the following LPC1100 devices:

- [LPC1100LV Family](#)
- [LPC1100XL Family](#)
- [LPC1120 Family](#)
- [LPC11A00 Family](#)
- [LPC11C00 Family](#)
- [LPC11D00 Family](#)
- [LPC11E00 Family](#)
- [LPC11U00 Family](#)

## LPC1100LV Family

- [LPC1101LVUK](#)
- [LPC1102LVUK](#)
- [LPC1112LVFHI33/103](#)
- [LPC1112LVFHN24/003](#)
- [LPC1114LVFHI33/303](#)
- [LPC1114LVFHN24/103](#)
- [LPC1114LVFHN24/303](#)

# LPC1101LVUK

Device Details	
CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV/LPC11xxLV.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xxLV.c
Family	LPC1100
Sub Family	LPC1100LV
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100LV_Small_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1101LVUK_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11xxLV_LPC111x_LV_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11xxLV_LPC111x_LV_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0	
LPC1101LVUK_DEVICE	
__LPC1100LV_SUBFAMILY	
__LPC1100_FAMILY	
__LPC1101LVUK__	

Memory Segments	
FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x100007FF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV
DeviceHeaderFile	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV/LPC11xxLV.h
DeviceLoaderFile	\$(TargetsDir)/LPC1100/Loader/LPC1100LV_Small_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/LPC1100/XML/LPC11xxLV_LPC111x_LV_Registers.xml
DeviceSystemFile	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xxLV.c
DeviceVectorsFile	\$(TargetsDir)/LPC1100/Source/LPC11xxLV_LPC111x_LV_Vectors.s
DeviceFamily	LPC1100



**Project Macros**

DeviceSubFamily=LPC1100LV

# LPC1102LVUK

Device Details	
CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV/LPC11xxLV.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xxLV.c
Family	LPC1100
Sub Family	LPC1100LV
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100LV_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1102LVUK_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11xxLV_LPC111x_LV_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11xxLV_LPC111x_LV_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0	
LPC1102LVUK_DEVICE	
__LPC1100LV_SUBFAMILY	
__LPC1100_FAMILY	
__LPC1102LVUK__	

Memory Segments	
FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV
DeviceHeaderFile	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV/LPC11xxLV.h
DeviceLoaderFile	\$(TargetsDir)/LPC1100/Loader/LPC1100LV_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/LPC1100/XML/LPC11xxLV_LPC111x_LV_Registers.xml
DeviceSystemFile	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xxLV.c
DeviceVectorsFile	\$(TargetsDir)/LPC1100/Source/LPC11xxLV_LPC111x_LV_Vectors.s
DeviceFamily	LPC1100
DeviceSubFamily	LPC1100LV

# LPC1112LVFHI33/103

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV/LPC11xxLV.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xxLV.c
Family	LPC1100
Sub Family	LPC1100LV
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100LV_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1112LVFHI33_103_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11xxLV_LPC111x_LV_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11xxLV_LPC111x_LV_Vectors.s

## Preprocessor Definitions

```

ARM_MATH_CM0
LPC1112LVFHI33_103_DEVICE
__LPC1100LV_SUBFAMILY
__LPC1100_FAMILY
__LPC1112LVFHI33_103__

```

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10000FFF

## Project Macros

```

DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV/LPC11xxLV.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100LV_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11xxLV_LPC111x_LV_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xxLV.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11xxLV_LPC111x_LV_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100LV

```

## LPC1112LVFHN24/003

### Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV/LPC11xxLV.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xxLV.c
Family	LPC1100
Sub Family	LPC1100LV
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100LV_Small_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1112LVFHN24_003_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11xxLV_LPC111x_LV_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11xxLV_LPC111x_LV_Vectors.s

### Preprocessor Definitions

```
ARM_MATH_CM0
LPC1112LVFHN24_003_DEVICE
__LPC1100LV_SUBFAMILY
__LPC1100_FAMILY
__LPC1112LVFHN24_003__
```

### Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x100007FF

### Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV/LPC11xxLV.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100LV_Small_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11xxLV_LPC111x_LV_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xxLV.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11xxLV_LPC111x_LV_Vectors.s
DeviceFamily=LPC1100
```

**Project Macros**

DeviceSubFamily=LPC1100LV

# LPC1114LVFHI33/303

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV/LPC11xxLV.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xxLV.c
Family	LPC1100
Sub Family	LPC1100LV
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100LV_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1114LVFHI33_303_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11xxLV_LPC111x_LV_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11xxLV_LPC111x_LV_Vectors.s

## Preprocessor Definitions

```

ARM_MATH_CM0
LPC1114LVFHI33_303_DEVICE
__LPC1100LV_SUBFAMILY
__LPC1100_FAMILY
__LPC1114LVFHI33_303__

```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```

DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV/LPC11xxLV.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100LV_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11xxLV_LPC111x_LV_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xxLV.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11xxLV_LPC111x_LV_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100LV

```

## LPC1114LVFHN24/103

Device Details	
CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV/LPC11xxLV.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xxLV.c
Family	LPC1100
Sub Family	LPC1100LV
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100LV_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1114LVFHN24_103_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11xxLV_LPC111x_LV_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11xxLV_LPC111x_LV_Vectors.s

Preprocessor Definitions
ARM_MATH_CM0
LPC1114LVFHN24_103_DEVICE
__LPC1100LV_SUBFAMILY
__LPC1100_FAMILY
__LPC1114LVFHN24_103__

Memory Segments	
FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10000FFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV
DeviceHeaderFile=\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV/LPC11xxLV.h
DeviceLoaderFile=\$(TargetsDir)/LPC1100/Loader/LPC1100LV_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC1100/XML/LPC11xxLV_LPC111x_LV_Registers.xml
DeviceSystemFile=\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xxLV.c
DeviceVectorsFile=\$(TargetsDir)/LPC1100/Source/LPC11xxLV_LPC111x_LV_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100LV

# LPC1114LVFHN24/303

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV/LPC11xxLV.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xxLV.c
Family	LPC1100
Sub Family	LPC1100LV
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100LV_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1114LVFHN24_303_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11xxLV_LPC111x_LV_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11xxLV_LPC111x_LV_Vectors.s

## Preprocessor Definitions

```

ARM_MATH_CM0
LPC1114LVFHN24_303_DEVICE
__LPC1100LV_SUBFAMILY
__LPC1100_FAMILY
__LPC1114LVFHN24_303__

```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```

DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xxLV/LPC11xxLV.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100LV_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11xxLV_LPC111x_LV_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xxLV.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11xxLV_LPC111x_LV_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100LV

```



## LPC1100XL Family

- [LPC1102UK](#)
- [LPC1104UK](#)
- [LPC1110FD20](#)
- [LPC1111FDH20/002](#)
- [LPC1111FHN33/101](#)
- [LPC1111FHN33/102](#)
- [LPC1111FHN33/201](#)
- [LPC1111FHN33/202](#)
- [LPC1111FHN33/103](#)
- [LPC1111FHN33/203](#)
- [LPC1112FD20/102](#)
- [LPC1112FDH20/102](#)
- [LPC1112FDH28/102](#)
- [LPC1112FHI33/202](#)
- [LPC1112FHI33/203](#)
- [LPC1112FHN24/202](#)
- [LPC1112FHN33/101](#)
- [LPC1112FHN33/201](#)
- [LPC1112FHN33/102](#)
- [LPC1112FHN33/202](#)
- [LPC1112FHN33/103](#)
- [LPC1112FHN33/203](#)
- [LPC1112JHI33/203](#)
- [LPC1113FBD48/301](#)
- [LPC1113FBD48/302](#)
- [LPC1113FBD48/303](#)
- [LPC1113FHN33/201](#)
- [LPC1113FHN33/301](#)
- [LPC1113FHN33/202](#)
- [LPC1113FHN33/302](#)
- [LPC1113FHN33/203](#)
- [LPC1113FHN33/303](#)
- [LPC1114FBD48/301](#)
- [LPC1114FBD48/302](#)
- [LPC1114FBD48/303](#)
- [LPC1114FBD48/323](#)
- [LPC1114FBD48/333](#)
- [LPC1114FDH28/102](#)

- [LPC1114FHI33/302](#)
- [LPC1114FHI33/303](#)
- [LPC1114FHN33/201](#)
- [LPC1114FHN33/301](#)
- [LPC1114FHN33/202](#)
- [LPC1114FHN33/302](#)
- [LPC1114FHN33/203](#)
- [LPC1114FHN33/303](#)
- [LPC1114FHN33/333](#)
- [LPC1114FN28/102](#)
- [LPC1114JHN33/333](#)
- [LPC1115FBD48/303](#)
- [LPC1115FET48/303](#)
- [LPC1115JBD48/303](#)
- [LPC1115JET48/303](#)

# LPC1102UK

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1102UK_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC1102_04_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC1102_04_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1102UK_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1102UK__
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC1102_04_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC1102_04_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

# LPC1104UK

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1104UK_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC1102_04_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC1102_04_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1104UK_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1104UK__
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC1102_04_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC1102_04_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

# LPC1110FD20

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Small_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1110FD20_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1110FD20_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1110FD20__
```

## Memory Segments

FLASH	0x00000000 - 0x00000FFF
RAM	0x10000000 - 0x100003FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Small_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

## LPC1111FDH20/002

### Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Small_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1111FDH20_002_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

### Preprocessor Definitions

```
ARM_MATH_CM0
LPC1111FDH20_002_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1111FDH20_002__
```

### Memory Segments

FLASH	0x00000000 - 0x00001FFF
RAM	0x10000000 - 0x100007FF

### Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Small_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

# LPC1111FHN33/101

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Small_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1111FHN33_101_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1111FHN33_101_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1111FHN33_101__
```

## Memory Segments

FLASH	0x00000000 - 0x00001FFF
RAM	0x10000000 - 0x100007FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Small_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

## LPC1111FHN33/102

### Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Small_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1111FHN33_102_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

### Preprocessor Definitions

```
ARM_MATH_CM0
LPC1111FHN33_102_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1111FHN33_102__
```

### Memory Segments

FLASH	0x00000000 - 0x00001FFF
RAM	0x10000000 - 0x100007FF

### Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Small_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```



# LPC1111FHN33/201

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1111FHN33_201_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1111FHN33_201_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1111FHN33_201__
```

## Memory Segments

FLASH	0x00000000 - 0x00001FFF
RAM	0x10000000 - 0x10000FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

## LPC1111FHN33/202

Device Details	
CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1111FHN33_202_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0	
LPC1111FHN33_202_DEVICE	
__LPC1100XL_SUBFAMILY	
__LPC1100_FAMILY	
__LPC1111FHN33_202__	

Memory Segments	
FLASH	0x00000000 - 0x00001FFF
RAM	0x10000000 - 0x10000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily	LPC1100
DeviceSubFamily	LPC1100XL

# LPC1111FHN33/103

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Small_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1111FHN33_103_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1111FHN33_103_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1111FHN33_103__
```

## Memory Segments

FLASH	0x00000000 - 0x00001FFF
RAM	0x10000000 - 0x100007FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Small_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

## LPC1111FHN33/203

### Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1111FHN33_203_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

### Preprocessor Definitions

```
ARM_MATH_CM0
LPC1111FHN33_203_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1111FHN33_203__
```

### Memory Segments

FLASH	0x00000000 - 0x00001FFF
RAM	0x10000000 - 0x10000FFF

### Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

# LPC1112FD20/102

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1112FD20_102_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1112FD20_102_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1112FD20_102__
```

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10000FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

# LPC1112FDH20/102

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1112FDH20_102_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```

ARM_MATH_CM0
LPC1112FDH20_102_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1112FDH20_102__

```

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10000FFF

## Project Macros

```

DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL

```

# LPC1112FDH28/102

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1112FDH28_102_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1112FDH28_102_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1112FDH28_102__
```

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10000FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

# LPC1112FHI33/202

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1112FHI33_202_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1112FHI33_202_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1112FHI33_202__
```

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10000FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```



## LPC1112FHI33/203

### Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1112FHI33_203_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

### Preprocessor Definitions

```
ARM_MATH_CM0
LPC1112FHI33_203_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1112FHI33_203__
```

### Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10000FFF

### Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

# LPC1112FHN24/202

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1112FHN24_202_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1112FHN24_202_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1112FHN24_202__
```

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10000FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

# LPC1112FHN33/101

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Small_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1112FHN33_101_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```

ARM_MATH_CM0
LPC1112FHN33_101_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1112FHN33_101__

```

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x100007FF

## Project Macros

```

DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Small_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL

```

# LPC1112FHN33/201

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1112FHN33_201_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1112FHN33_201_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1112FHN33_201__
```

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10000FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

# LPC1112FHN33/102

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Small_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1112FHN33_102_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1112FHN33_102_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1112FHN33_102__
```

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x100007FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Small_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

# LPC1112FHN33/202

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1112FHN33_202_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1112FHN33_202_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1112FHN33_202__
```

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10000FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

## LPC1112FHN33/103

### Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Small_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1112FHN33_103_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

### Preprocessor Definitions

```
ARM_MATH_CM0
LPC1112FHN33_103_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1112FHN33_103__
```

### Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x100007FF

### Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Small_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

# LPC1112FHN33/203

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1112FHN33_203_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1112FHN33_203_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1112FHN33_203__
```

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10000FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```



# LPC1112JHI33/203

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1112JHI33_203_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```

ARM_MATH_CM0
LPC1112JHI33_203_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1112JHI33_203__

```

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10000FFF

## Project Macros

```

DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL

```

# LPC1113FBD48/301

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1113FBD48_301_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1113FBD48_301_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1113FBD48_301__
```

## Memory Segments

FLASH	0x00000000 - 0x00005FFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

## LPC1113FBD48/302

### Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1113FBD48_302_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

### Preprocessor Definitions

```
ARM_MATH_CM0
LPC1113FBD48_302_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1113FBD48_302__
```

### Memory Segments

FLASH	0x00000000 - 0x00005FFF
RAM	0x10000000 - 0x10001FFF

### Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

## LPC1113FBD48/303

Device Details	
CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1113FBD48_303_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0	
LPC1113FBD48_303_DEVICE	
__LPC1100XL_SUBFAMILY	
__LPC1100_FAMILY	
__LPC1113FBD48_303__	

Memory Segments	
FLASH	0x00000000 - 0x00005FFF
RAM	0x10000000 - 0x10001FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily	LPC1100
DeviceSubFamily	LPC1100XL

# LPC1113FHN33/201

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1113FHN33_201_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1113FHN33_201_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1113FHN33_201__
```

## Memory Segments

FLASH	0x00000000 - 0x00005FFF
RAM	0x10000000 - 0x10000FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

# LPC1113FHN33/301

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1113FHN33_301_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1113FHN33_301_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1113FHN33_301__
```

## Memory Segments

FLASH	0x00000000 - 0x00005FFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

# LPC1113FHN33/202

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1113FHN33_202_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1113FHN33_202_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1113FHN33_202__
```

## Memory Segments

FLASH	0x00000000 - 0x00005FFF
RAM	0x10000000 - 0x10000FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

# LPC1113FHN33/302

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1113FHN33_302_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1113FHN33_302_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1113FHN33_302__
```

## Memory Segments

FLASH	0x00000000 - 0x00005FFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```



## LPC1113FHN33/203

### Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1113FHN33_203_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

### Preprocessor Definitions

```
ARM_MATH_CM0
LPC1113FHN33_203_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1113FHN33_203__
```

### Memory Segments

FLASH	0x00000000 - 0x00005FFF
RAM	0x10000000 - 0x10000FFF

### Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

## LPC1113FHN33/303

### Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1113FHN33_303_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

### Preprocessor Definitions

```
ARM_MATH_CM0
LPC1113FHN33_303_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1113FHN33_303__
```

### Memory Segments

FLASH	0x00000000 - 0x00005FFF
RAM	0x10000000 - 0x10001FFF

### Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

# LPC1114FBD48/301

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1114FBD48_301_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1114FBD48_301_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1114FBD48_301__
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

## LPC1114FBD48/302

Device Details	
CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1114FBD48_302_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0	
LPC1114FBD48_302_DEVICE	
__LPC1100XL_SUBFAMILY	
__LPC1100_FAMILY	
__LPC1114FBD48_302__	

Memory Segments	
FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily	LPC1100
DeviceSubFamily	LPC1100XL

## LPC1114FBD48/303

### Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1114FBD48_303_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

### Preprocessor Definitions

```
ARM_MATH_CM0
LPC1114FBD48_303_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1114FBD48_303__
```

### Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF

### Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

## LPC1114FBD48/323

### Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1114FBD48_323_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

### Preprocessor Definitions

```
ARM_MATH_CM0
LPC1114FBD48_323_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1114FBD48_323__
```

### Memory Segments

FLASH	0x00000000 - 0x0000BFFF
RAM	0x10000000 - 0x10001FFF

### Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

# LPC1114FBD48/333

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1114FBD48_333_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```

ARM_MATH_CM0
LPC1114FBD48_333_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1114FBD48_333__

```

## Memory Segments

FLASH	0x00000000 - 0x0000DFFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```

DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL

```

# LPC1114FDH28/102

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1114FDH28_102_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1114FDH28_102_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1114FDH28_102__
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10000FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```



## LPC1114FHI33/302

### Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1114FHI33_302_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

### Preprocessor Definitions

```
ARM_MATH_CM0
LPC1114FHI33_302_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1114FHI33_302__
```

### Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF

### Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

# LPC1114FHI33/303

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1114FHI33_303_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1114FHI33_303_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1114FHI33_303__
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

# LPC1114FHN33/201

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1114FHN33_201_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1114FHN33_201_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1114FHN33_201__
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10000FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

# LPC1114FHN33/301

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1114FHN33_301_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1114FHN33_301_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1114FHN33_301__
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

## LPC1114FHN33/202

### Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1114FHN33_202_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

### Preprocessor Definitions

```
ARM_MATH_CM0
LPC1114FHN33_202_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1114FHN33_202__
```

### Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10000FFF

### Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

# LPC1114FHN33/302

Device Details	
CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1114FHN33_302_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0	
LPC1114FHN33_302_DEVICE	
__LPC1100XL_SUBFAMILY	
__LPC1100_FAMILY	
__LPC1114FHN33_302__	

Memory Segments	
FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily	LPC1100
DeviceSubFamily	LPC1100XL

# LPC1114FHN33/203

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1114FHN33_203_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```

ARM_MATH_CM0
LPC1114FHN33_203_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1114FHN33_203__

```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10000FFF

## Project Macros

```

DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL

```

## LPC1114FHN33/303

### Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1114FHN33_303_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

### Preprocessor Definitions

```
ARM_MATH_CM0
LPC1114FHN33_303_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1114FHN33_303__
```

### Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF

### Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```



# LPC1114FHN33/333

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1114FHN33_333_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1114FHN33_333_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1114FHN33_333__
```

## Memory Segments

FLASH	0x00000000 - 0x0000DFFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

## LPC1114FN28/102

### Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1114FN28_102_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

### Preprocessor Definitions

ARM\_MATH\_CM0  
 LPC1114FN28\_102\_DEVICE  
 \_\_LPC1100XL\_SUBFAMILY  
 \_\_LPC1100\_FAMILY  
 \_\_LPC1114FN28\_102\_\_

### Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10000FFF

### Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx  
 DeviceHeaderFile=\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h  
 DeviceLoaderFile=\$(TargetsDir)/LPC1100/Loader/LPC1100XL\_Loader.elf  
 DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC1100/XML/LPC111x\_Registers.xml  
 DeviceSystemFile=\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system\_LPC11xx.c  
 DeviceVectorsFile=\$(TargetsDir)/LPC1100/Source/LPC111x\_Vectors.s  
 DeviceFamily=LPC1100  
 DeviceSubFamily=LPC1100XL

# LPC1114JHN33/333

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1114JHN33_333_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1114JHN33_333_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1114JHN33_333__
```

## Memory Segments

FLASH	0x00000000 - 0x0000DFFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

## LPC1115FBD48/303

Device Details	
CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1115FBD48_303_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0	
LPC1115FBD48_303_DEVICE	
__LPC1100XL_SUBFAMILY	
__LPC1100_FAMILY	
__LPC1115FBD48_303__	

Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x10000000 - 0x10001FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily	LPC1100
DeviceSubFamily	LPC1100XL

# LPC1115FET48/303

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1115FET48_303_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1115FET48_303_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1115FET48_303__
```

## Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

# LPC1115JBD48/303

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1115JBD48_303_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC1115JBD48_303_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1115JBD48_303__
```

## Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL
```

# LPC1115JET48/303

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC1100XL
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1115JET48_303_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s

## Preprocessor Definitions

```

ARM_MATH_CM0
LPC1115JET48_303_DEVICE
__LPC1100XL_SUBFAMILY
__LPC1100_FAMILY
__LPC1115JET48_303__

```

## Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```

DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC1100XL_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC111x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC111x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC1100XL

```

## LPC1120 Family

- [LPC1124JBD48/303](#)
- [LPC1125JBD48/303](#)



# LPC1124JBD48/303

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC112x/LPC112x.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC112x
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC112x.c
Family	LPC1100
Sub Family	LPC1120
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1120_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1124JBD48_303_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC112x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC112x_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM0  
LPC1124  
LPC1124JBD48\_303\_DEVICE  
\_\_LPC1100\_FAMILY  
\_\_LPC1120\_SUBFAMILY

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC112x  
DeviceHeaderFile=\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC112x/LPC112x.h  
DeviceLoaderFile=\$(TargetsDir)/LPC1100/Loader/LPC1120\_Loader.elf  
DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC1100/XML/LPC112x\_Registers.xml  
DeviceSystemFile=\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system\_LPC112x.c  
DeviceVectorsFile=\$(TargetsDir)/LPC1100/Source/LPC112x\_Vectors.s  
DeviceFamily=LPC1100  
DeviceSubFamily=LPC1120

# LPC1125JBD48/303

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC112x/LPC112x.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC112x
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC112x.c
Family	LPC1100
Sub Family	LPC1120
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC1120_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC1125JBD48_303_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC112x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC112x_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM0  
 LPC1125  
 LPC1125JBD48\_303\_DEVICE  
 \_\_LPC1100\_FAMILY  
 \_\_LPC1120\_SUBFAMILY

## Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC112x  
 DeviceHeaderFile=\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC112x/LPC112x.h  
 DeviceLoaderFile=\$(TargetsDir)/LPC1100/Loader/LPC1120\_Loader.elf  
 DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC1100/XML/LPC112x\_Registers.xml  
 DeviceSystemFile=\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system\_LPC112x.c  
 DeviceVectorsFile=\$(TargetsDir)/LPC1100/Source/LPC112x\_Vectors.s  
 DeviceFamily=LPC1100  
 DeviceSubFamily=LPC1120

## LPC11A00 Family

- [LPC11A02UK](#)
- [LPC11A04UK](#)
- [LPC11A11FHN33/001](#)
- [LPC11A12FBD48/101](#)
- [LPC11A12FHN33/101](#)
- [LPC11A13FHI33/201](#)
- [LPC11A14FBD48/301](#)
- [LPC11A14FHN33/301](#)

# LPC11A02UK

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx/LPC11Axx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Axx.c
Family	LPC1100
Sub Family	LPC11A00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11A00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11A02UK_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Axx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Axx_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM0

LPC11A02UK\_DEVICE

\_\_LPC1100\_FAMILY

\_\_LPC11A00\_SUBFAMILY

\_\_LPC11A02UK\_\_

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10000FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx

DeviceHeaderFile=\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx/LPC11Axx.h

DeviceLoaderFile=\$(TargetsDir)/LPC1100/Loader/LPC11A00\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC1100/XML/LPC11Axx\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system\_LPC11Axx.c

DeviceVectorsFile=\$(TargetsDir)/LPC1100/Source/LPC11Axx\_Vectors.s

DeviceFamily=LPC1100

DeviceSubFamily=LPC11A00

# LPC11A04UK

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx/LPC11Axx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Axx.c
Family	LPC1100
Sub Family	LPC11A00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11A00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11A04UK_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Axx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Axx_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM0

LPC11A04UK\_DEVICE

\_\_LPC1100\_FAMILY

\_\_LPC11A00\_SUBFAMILY

\_\_LPC11A04UK\_\_

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx

DeviceHeaderFile=\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx/LPC11Axx.h

DeviceLoaderFile=\$(TargetsDir)/LPC1100/Loader/LPC11A00\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC1100/XML/LPC11Axx\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system\_LPC11Axx.c

DeviceVectorsFile=\$(TargetsDir)/LPC1100/Source/LPC11Axx\_Vectors.s

DeviceFamily=LPC1100

DeviceSubFamily=LPC11A00

# LPC11A11FHN33/001

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx/LPC11Axx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Axx.c
Family	LPC1100
Sub Family	LPC11A00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11A00_Small_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11A11FHN33_001_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Axx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Axx_Vectors.s

## Preprocessor Definitions

```

ARM_MATH_CM0
LPC11A11FHN33_001_DEVICE
__LPC1100_FAMILY
__LPC11A00_SUBFAMILY
__LPC11A11FHN33_001__

```

## Memory Segments

FLASH	0x00000000 - 0x00001FFF
RAM	0x10000000 - 0x100007FF

## Project Macros

```

DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx/LPC11Axx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11A00_Small_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Axx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Axx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Axx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11A00

```

# LPC11A12FBD48/101

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx/LPC11Axx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Axx.c
Family	LPC1100
Sub Family	LPC11A00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11A00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11A12FBD48_101_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Axx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Axx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11A12FBD48_101_DEVICE
__LPC1100_FAMILY
__LPC11A00_SUBFAMILY
__LPC11A12FBD48_101__
```

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10000FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx/LPC11Axx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11A00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Axx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Axx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Axx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11A00
```

# LPC11A12FHN33/101

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx/LPC11Axx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Axx.c
Family	LPC1100
Sub Family	LPC11A00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11A00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11A12FHN33_101_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Axx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Axx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11A12FHN33_101_DEVICE
__LPC1100_FAMILY
__LPC11A00_SUBFAMILY
__LPC11A12FHN33_101__
```

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10000FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx/LPC11Axx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11A00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Axx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Axx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Axx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11A00
```



# LPC11A13FHI33/201

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx/LPC11Axx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Axx.c
Family	LPC1100
Sub Family	LPC11A00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11A00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11A13FHI33_201_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Axx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Axx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11A13FHI33_201_DEVICE
__LPC1100_FAMILY
__LPC11A00_SUBFAMILY
__LPC11A13FHI33_201__
```

## Memory Segments

FLASH	0x00000000 - 0x00005FFF
RAM	0x10000000 - 0x100017FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx/LPC11Axx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11A00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Axx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Axx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Axx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11A00
```

# LPC11A14FBD48/301

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx/LPC11Axx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Axx.c
Family	LPC1100
Sub Family	LPC11A00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11A00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11A14FBD48_301_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Axx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Axx_Vectors.s

## Preprocessor Definitions

```

ARM_MATH_CM0
LPC11A14FBD48_301_DEVICE
__LPC1100_FAMILY
__LPC11A00_SUBFAMILY
__LPC11A14FBD48_301__

```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```

DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx/LPC11Axx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11A00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Axx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Axx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Axx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11A00

```

# LPC11A14FHN33/301

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx/LPC11Axx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Axx.c
Family	LPC1100
Sub Family	LPC11A00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11A00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11A14FHN33_301_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Axx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Axx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11A14FHN33_301_DEVICE
__LPC1100_FAMILY
__LPC11A00_SUBFAMILY
__LPC11A14FHN33_301__
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Axx/LPC11Axx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11A00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Axx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Axx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Axx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11A00
```

## LPC11C00 Family

- [LPC11C12FBD48/301](#)
- [LPC11C14FBD48/301](#)
- [LPC11C22FBD48/301](#)
- [LPC11C24FBD48/301](#)

# LPC11C12FBD48/301

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC11C00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11C00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11C12FBD48_301_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Cxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Cxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11C12FBD48_301_DEVICE
__LPC1100_FAMILY
__LPC11C00_SUBFAMILY
__LPC11C12FBD48_301__
```

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11C00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Cxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Cxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11C00
```

# LPC11C14FBD48/301

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC11C00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11C00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11C14FBD48_301_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Cxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Cxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11C14FBD48_301_DEVICE
__LPC1100_FAMILY
__LPC11C00_SUBFAMILY
__LPC11C14FBD48_301__
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11C00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Cxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Cxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11C00
```

# LPC11C22FBD48/301

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC11C00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11C00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11C22FBD48_301_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Cxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Cxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11C22FBD48_301_DEVICE
__LPC1100_FAMILY
__LPC11C00_SUBFAMILY
__LPC11C22FBD48_301__
```

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11C00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Cxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Cxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11C00
```

# LPC11C24FBD48/301

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC11C00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11C00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11C24FBD48_301_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Cxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Cxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11C24FBD48_301_DEVICE
__LPC1100_FAMILY
__LPC11C00_SUBFAMILY
__LPC11C24FBD48_301__
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11C00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Cxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Cxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11C00
```



# LPC11D00 Family

- [LPC11D14FBD100/302](#)

## LPC11D14FBD100/302

### Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
Family	LPC1100
Sub Family	LPC11D00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11D00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11D14FBD100_302_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11D14_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11D14_Vectors.s

### Preprocessor Definitions

```
ARM_MATH_CM0
LPC11D14FBD100_302_DEVICE
__LPC1100_FAMILY
__LPC11D00_SUBFAMILY
__LPC11D14FBD100_302__
```

### Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF

### Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11xx/LPC11xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11D00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11D14_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11D14_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11D00
```

## LPC11E00 Family

- [LPC11E11FHN33/101](#)
- [LPC11E12FBD48/201](#)
- [LPC11E13FBD48/301](#)
- [LPC11E14FBD48/401](#)
- [LPC11E14FBD64/401](#)
- [LPC11E14FHN33/401](#)
- [LPC11E36FBD64/501](#)
- [LPC11E36FHN33/501](#)
- [LPC11E37FBD48/501](#)
- [LPC11E37FBD64/501](#)
- [LPC11E37HFBD64/401](#)
- [LPC11E66JBD48](#)
- [LPC11E67JBD100](#)
- [LPC11E67JBD48](#)
- [LPC11E67JBD64](#)
- [LPC11E68JBD100](#)
- [LPC11E68JBD48](#)
- [LPC11E68JBD64](#)

# LPC11E11FHN33/101

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11Exx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Exx.c
Family	LPC1100
Sub Family	LPC11E00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11E11FHN33_101_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Exx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Exx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11E11FHN33_101_DEVICE
__LPC1100_FAMILY
__LPC11E00_SUBFAMILY
__LPC11E11FHN33_101__
```

## Memory Segments

FLASH	0x00000000 - 0x00001FFF
RAM	0x10000000 - 0x10000FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11Exx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Exx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Exx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Exx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11E00
```

# LPC11E12FBD48/201

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11Exx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Exx.c
Family	LPC1100
Sub Family	LPC11E00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11E12FBD48_201_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Exx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Exx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11E12FBD48_201_DEVICE
__LPC1100_FAMILY
__LPC11E00_SUBFAMILY
__LPC11E12FBD48_201__
```

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x100017FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11Exx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Exx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Exx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Exx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11E00
```

# LPC11E13FBD48/301

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11Exx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Exx.c
Family	LPC1100
Sub Family	LPC11E00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11E13FBD48_301_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Exx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Exx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11E13FBD48_301_DEVICE
__LPC1100_FAMILY
__LPC11E00_SUBFAMILY
__LPC11E13FBD48_301__
```

## Memory Segments

FLASH	0x00000000 - 0x00005FFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11Exx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Exx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Exx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Exx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11E00
```

# LPC11E14FBD48/401

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11Exx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Exx.c
Family	LPC1100
Sub Family	LPC11E00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11E14FBD48_401_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Exx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Exx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11E14FBD48_401_DEVICE
__LPC1100_FAMILY
__LPC11E00_SUBFAMILY
__LPC11E14FBD48_401__
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x100027FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11Exx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Exx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Exx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Exx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11E00
```

# LPC11E14FBD64/401

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11Exx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Exx.c
Family	LPC1100
Sub Family	LPC11E00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11E14FBD64_401_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Exx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Exx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11E14FBD64_401_DEVICE
__LPC1100_FAMILY
__LPC11E00_SUBFAMILY
__LPC11E14FBD64_401__
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x100027FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11Exx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Exx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Exx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Exx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11E00
```



# LPC11E14FHN33/401

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11Exx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Exx.c
Family	LPC1100
Sub Family	LPC11E00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11E14FHN33_401_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Exx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Exx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11E14FHN33_401_DEVICE
__LPC1100_FAMILY
__LPC11E00_SUBFAMILY
__LPC11E14FHN33_401__
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x100027FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11Exx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Exx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Exx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Exx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11E00
```

# LPC11E36FBD64/501

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11Exx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Exx.c
Family	LPC1100
Sub Family	LPC11E00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11E36FBD64_501_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Exx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Exx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11E36FBD64_501_DEVICE
__LPC1100_FAMILY
__LPC11E00_SUBFAMILY
__LPC11E36FBD64_501__
```

## Memory Segments

FLASH	0x00000000 - 0x00017FFF
RAM	0x10000000 - 0x10001FFF
RAM2	0x20000000 - 0x200007FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11Exx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Exx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Exx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Exx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11E00
```

# LPC11E36FHN33/501

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11Exx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Exx.c
Family	LPC1100
Sub Family	LPC11E00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11E36FHN33_501_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Exx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Exx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11E36FHN33_501_DEVICE
__LPC1100_FAMILY
__LPC11E00_SUBFAMILY
__LPC11E36FHN33_501__
```

## Memory Segments

FLASH	0x00000000 - 0x00017FFF
RAM	0x10000000 - 0x10001FFF
RAM2	0x20000000 - 0x200007FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11Exx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Exx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Exx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Exx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11E00
```

# LPC11E37FBD48/501

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11Exx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Exx.c
Family	LPC1100
Sub Family	LPC11E00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11E37FBD48_501_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Exx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Exx_Vectors.s

## Preprocessor Definitions

```

ARM_MATH_CM0
LPC11E37FBD48_501_DEVICE
__LPC1100_FAMILY
__LPC11E00_SUBFAMILY
__LPC11E37FBD48_501__

```

## Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x10000000 - 0x10001FFF
RAM2	0x20000000 - 0x200007FF

## Project Macros

```

DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11Exx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Exx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Exx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Exx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11E00

```

# LPC11E37FBD64/501

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11Exx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Exx.c
Family	LPC1100
Sub Family	LPC11E00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11E37FBD64_501_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Exx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Exx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11E37FBD64_501_DEVICE
__LPC1100_FAMILY
__LPC11E00_SUBFAMILY
__LPC11E37FBD64_501__
```

## Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x10000000 - 0x10001FFF
RAM2	0x20000000 - 0x200007FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11Exx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Exx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Exx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Exx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11E00
```

# LPC11E37HFBD64/401

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11Exx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Exx.c
Family	LPC1100
Sub Family	LPC11E00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11E37HFBD64_401_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Exx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Exx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11E37HFBD64_401_DEVICE
__LPC1100_FAMILY
__LPC11E00_SUBFAMILY
__LPC11E37HFBD64_401__
```

## Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x10000000 - 0x10001FFF
RAM2	0x20000000 - 0x200007FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11Exx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Exx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Exx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Exx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11E00
```

# LPC11E66JBD48

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11E6x.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11E6x.c
Family	LPC1100
Sub Family	LPC11E00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11E66JBD48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11E6x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11E6x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
LPC11E66JBD48_DEVICE
__LPC1100_FAMILY
__LPC11E00_SUBFAMILY
__LPC11E66JBD48__
```

## Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x10000000 - 0x10001FFF
RAM2	0x20000000 - 0x200007FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11E6x.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11E6x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11E6x.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11E6x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11E00
```

# LPC11E67JBD100

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11E6x.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11E6x.c
Family	LPC1100
Sub Family	LPC11E00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11E67JBD100_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11E6x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11E6x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
LPC11E67JBD100_DEVICE
__LPC1100_FAMILY
__LPC11E00_SUBFAMILY
__LPC11E67JBD100__
```

## Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x10000000 - 0x10003FFF
RAM2	0x20000000 - 0x200007FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11E6x.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11E6x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11E6x.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11E6x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11E00
```



# LPC11E67JBD48

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11E6x.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11E6x.c
Family	LPC1100
Sub Family	LPC11E00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11E67JBD48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11E6x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11E6x_Vectors.s

## Preprocessor Definitions

```

ARM_MATH_CM0PLUS
LPC11E67JBD48_DEVICE
__LPC1100_FAMILY
__LPC11E00_SUBFAMILY
__LPC11E67JBD48__

```

## Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x10000000 - 0x10003FFF
RAM2	0x20000000 - 0x200007FF

## Project Macros

```

DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11E6x.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11E6x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11E6x.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11E6x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11E00

```

# LPC11E67JBD64

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11E6x.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11E6x.c
Family	LPC1100
Sub Family	LPC11E00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11E67JBD64_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11E6x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11E6x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
LPC11E67JBD64_DEVICE
__LPC1100_FAMILY
__LPC11E00_SUBFAMILY
__LPC11E67JBD64__
```

## Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x10000000 - 0x10003FFF
RAM2	0x20000000 - 0x200007FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11E6x.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11E6x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11E6x.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11E6x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11E00
```

# LPC11E68JBD100

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11E6x.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11E6x.c
Family	LPC1100
Sub Family	LPC11E00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11E68JBD100_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11E6x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11E6x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
LPC11E68JBD100_DEVICE
__LPC1100_FAMILY
__LPC11E00_SUBFAMILY
__LPC11E68JBD100__
```

## Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x10000000 - 0x10007FFF
RAM2	0x20000000 - 0x200007FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11E6x.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11E6x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11E6x.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11E6x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11E00
```

# LPC11E68JBD48

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11E6x.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11E6x.c
Family	LPC1100
Sub Family	LPC11E00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11E68JBD48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11E6x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11E6x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
LPC11E68JBD48_DEVICE
__LPC1100_FAMILY
__LPC11E00_SUBFAMILY
__LPC11E68JBD48__
```

## Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x10000000 - 0x10007FFF
RAM2	0x20000000 - 0x200007FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11E6x.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11E6x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11E6x.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11E6x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11E00
```

# LPC11E68JBD64

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11E6x.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11E6x.c
Family	LPC1100
Sub Family	LPC11E00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11E68JBD64_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11E6x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11E6x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
LPC11E68JBD64_DEVICE
__LPC1100_FAMILY
__LPC11E00_SUBFAMILY
__LPC11E68JBD64__
```

## Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x10000000 - 0x10007FFF
RAM2	0x20000000 - 0x200007FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Exx/LPC11E6x.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11E00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11E6x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11E6x.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11E6x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11E00
```

## LPC11U00 Family

- [LPC11U12FBD48/201](#)
- [LPC11U12FHN33/201](#)
- [LPC11U13FBD48/201](#)
- [LPC11U14FBD48/201](#)
- [LPC11U14FET48/201](#)
- [LPC11U14FHI33/201](#)
- [LPC11U14FHN33/201](#)
- [LPC11U23FBD48/301](#)
- [LPC11U24FBD48/301](#)
- [LPC11U24FBD48/401](#)
- [LPC11U24FBD64/401](#)
- [LPC11U24FET48/301](#)
- [LPC11U24FHI33/301](#)
- [LPC11U24FHN33/401](#)
- [LPC11U34FBD48/311](#)
- [LPC11U34FBD48/421](#)
- [LPC11U34FHN33/311](#)
- [LPC11U34FHN33/421](#)
- [LPC11U35FBD48/401](#)
- [LPC11U35FBD64/401](#)
- [LPC11U35FET48/501](#)
- [LPC11U35FHI33/501](#)
- [LPC11U35FHN33/401](#)
- [LPC11U36FBD48/401](#)
- [LPC11U36FBD64/401](#)
- [LPC11U37FBD48/401](#)
- [LPC11U37FBD64/501](#)
- [LPC11U37HFBD64/401](#)
- [LPC11U66JBD48](#)
- [LPC11U67JBD100](#)
- [LPC11U67JBD48](#)
- [LPC11U67JBD64](#)
- [LPC11U68JBD100](#)
- [LPC11U68JBD48](#)
- [LPC11U68JBD64](#)

# LPC11U12FBD48/201

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U12FBD48_201_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U12FBD48_201_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U12FBD48_201__
```

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10000FFF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U12FHN33/201

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U12FHN33_201_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U12FHN33_201_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U12FHN33_201__
```

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10000FFF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```



# LPC11U13FBD48/201

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U13FBD48_201_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U13FBD48_201_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U13FBD48_201__
```

## Memory Segments

FLASH	0x00000000 - 0x00005FFF
RAM	0x10000000 - 0x10000FFF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U14FBD48/201

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U14FBD48_201_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U14FBD48_201_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U14FBD48_201__
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10000FFF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U14FET48/201

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U14FET48_201_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U14FET48_201_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U14FET48_201__
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10000FFF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U14FHI33/201

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U14FHI33_201_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U14FHI33_201_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U14FHI33_201__
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10000FFF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U14FHN33/201

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U14FHN33_201_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U14FHN33_201_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U14FHN33_201__
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10000FFF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U23FBD48/301

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U23FBD48_301_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```

ARM_MATH_CM0
LPC11U23FBD48_301_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U23FBD48_301__

```

## Memory Segments

FLASH	0x00000000 - 0x00005FFF
RAM	0x10000000 - 0x100017FF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```

DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00

```

# LPC11U24FBD48/301

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U24FBD48_301_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U24FBD48_301_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U24FBD48_301__
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x100017FF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U24FBD48/401

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U24FBD48_401_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U24FBD48_401_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U24FBD48_401__
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```



# LPC11U24FBD64/401

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U24FBD64_401_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U24FBD64_401_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U24FBD64_401__
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U24FET48/301

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U24FET48_301_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U24FET48_301_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U24FET48_301__
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x100017FF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U24FHI33/301

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U24FHI33_301_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U24FHI33_301_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U24FHI33_301__
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x100017FF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U24FHN33/401

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U24FHN33_401_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U24FHN33_401_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U24FHN33_401__
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U34FBD48/311

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U34FBD48_311_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U34FBD48_311_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U34FBD48_311__
```

## Memory Segments

FLASH	0x00000000 - 0x00009FFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U34FBD48/421

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U34FBD48_421_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U34FBD48_421_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U34FBD48_421__
```

## Memory Segments

FLASH	0x00000000 - 0x0000BFFF
RAM	0x10000000 - 0x10001FFF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U34FHN33/311

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U34FHN33_311_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U34FHN33_311_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U34FHN33_311__
```

## Memory Segments

FLASH	0x00000000 - 0x00009FFF
RAM	0x10000000 - 0x10001FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U34FHN33/421

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U34FHN33_421_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U34FHN33_421_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U34FHN33_421__
```

## Memory Segments

FLASH	0x00000000 - 0x0000BFFF
RAM	0x10000000 - 0x10001FFF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```



# LPC11U35FBD48/401

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U35FBD48_401_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U35FBD48_401_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U35FBD48_401__
```

## Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x10000000 - 0x10001FFF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U35FBD64/401

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U35FBD64_401_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U35FBD64_401_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U35FBD64_401__
```

## Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x10000000 - 0x10001FFF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U35FET48/501

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U35FET48_501_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U35FET48_501_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U35FET48_501__
```

## Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x10000000 - 0x10001FFF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U35FHI33/501

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U35FHI33_501_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U35FHI33_501_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U35FHI33_501__
```

## Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x10000000 - 0x10001FFF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U35FHN33/401

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U35FHN33_401_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U35FHN33_401_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U35FHN33_401__
```

## Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x10000000 - 0x10001FFF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U36FBD48/401

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U36FBD48_401_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U36FBD48_401_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U36FBD48_401__
```

## Memory Segments

FLASH	0x00000000 - 0x00017FFF
RAM	0x10000000 - 0x10001FFF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U36FBD64/401

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U36FBD64_401_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U36FBD64_401_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U36FBD64_401__
```

## Memory Segments

FLASH	0x00000000 - 0x00017FFF
RAM	0x10000000 - 0x10001FFF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U37FBD48/401

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U37FBD48_401_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U37FBD48_401_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U37FBD48_401__
```

## Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x10000000 - 0x10001FFF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```



# LPC11U37FBD64/501

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U37FBD64_501_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U37FBD64_501_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U37FBD64_501__
```

## Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x10000000 - 0x10001FFF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U37HFBD64/401

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U37HFBD64_401_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0
LPC11U37HFBD64_401_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U37HFBD64_401__
```

## Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x10000000 - 0x10001FFF
RAM2	0x20004000 - 0x200047FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11Uxx_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U66JBD48

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11U6x.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11U6x.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U66JBD48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11U6x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11U6x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
LPC11U66JBD48_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U66JBD48__
```

## Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x10000000 - 0x10001FFF
RAM2	0x20000000 - 0x200007FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11U6x.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11U6x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11U6x.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11U6x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U67JBD100

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11U6x.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11U6x.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U67JBD100_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11U6x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11U6x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
LPC11U67JBD100_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U67JBD100__
```

## Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x10000000 - 0x10003FFF
RAM2	0x20000000 - 0x200007FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11U6x.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11U6x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11U6x.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11U6x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U67JBD48

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11U6x.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11U6x.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U67JBD48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11U6x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11U6x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
LPC11U67JBD48_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U67JBD48__
```

## Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x10000000 - 0x10003FFF
RAM2	0x20000000 - 0x200007FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11U6x.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11U6x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11U6x.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11U6x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U67JBD64

Device Details	
CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11U6x.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11U6x.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U67JBD64_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11U6x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11U6x_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
LPC11U67JBD64_DEVICE	
__LPC1100_FAMILY	
__LPC11U00_SUBFAMILY	
__LPC11U67JBD64__	

Memory Segments	
FLASH	0x00000000 - 0x0001FFFF
RAM	0x10000000 - 0x10003FFF
RAM2	0x20000000 - 0x200007FF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11U6x.h
DeviceLoaderFile	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/LPC1100/XML/LPC11U6x_Registers.xml
DeviceSystemFile	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11U6x.c
DeviceVectorsFile	\$(TargetsDir)/LPC1100/Source/LPC11U6x_Vectors.s
DeviceFamily	LPC1100
DeviceSubFamily	LPC11U00

# LPC11U68JBD100

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11U6x.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11U6x.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U68JBD100_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11U6x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11U6x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
LPC11U68JBD100_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U68JBD100__
```

## Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x10000000 - 0x10007FFF
RAM2	0x20000000 - 0x200007FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11U6x.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11U6x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11U6x.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11U6x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```

# LPC11U68JBD48

## Device Details

CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11U6x.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11U6x.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U68JBD48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11U6x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11U6x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
LPC11U68JBD48_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U68JBD48__
```

## Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x10000000 - 0x10007FFF
RAM2	0x20000000 - 0x200007FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11U6x.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11U6x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11U6x.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11U6x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```



# LPC11U68JBD64

Device Details	
CMSIS Header File	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11U6x.h
CMSIS Include Path	\$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
CMSIS System File	\$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11U6x.c
Family	LPC1100
Sub Family	LPC11U00
Loader File	\$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
Memory Map File	\$(TargetsDir)/LPC1100/XML/LPC11U68JBD64_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC1100/XML/LPC11U6x_Registers.xml
Vectors File	\$(TargetsDir)/LPC1100/Source/LPC11U6x_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
LPC11U68JBD64_DEVICE
__LPC1100_FAMILY
__LPC11U00_SUBFAMILY
__LPC11U68JBD64__
```

## Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x10000000 - 0x10007FFF
RAM2	0x20000000 - 0x200007FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1100/CMSIS/Device/Include/LPC11Uxx/LPC11U6x.h
DeviceLoaderFile=$(TargetsDir)/LPC1100/Loader/LPC11U00_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1100/XML/LPC11U6x_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1100/CMSIS/Device/Source/system_LPC11U6x.c
DeviceVectorsFile=$(TargetsDir)/LPC1100/Source/LPC11U6x_Vectors.s
DeviceFamily=LPC1100
DeviceSubFamily=LPC11U00
```